

Second Working Group Meeting on Logic Locking and Anti-Trojan Solutions

6PM-10PM Monday, November 4th, 2019

Westminster III (Main Room) & Westminster II, The Westin Westminster

Organizers: Swarup Bhunia, Jeyavijayan Rajendran, and Yier Jin

6:00 - 6:30PM	Registration @ Westminster III
Opening	Welcome and Opening Statements <i>Ken Plaks (DARPA)</i> <i>Swarup Bhunia (UF)</i>
6:30 - 6:50PM	Dinner Break
6:50 - 7:10PM	Summary of the First Working Group Meeting <i>Yier Jin (UF)</i>
7:10 - 7:55PM	<i>Mike Borza (Synopsys)</i> <i>AISS and More</i>
	<i>John Hallman (OneSpin)</i>
	<i>Matthew French (USC-ISI)</i> Approaches for Obfuscation Metrics
7:55 - 8:10PM	Break
8:10 - 8:40PM	<i>Alric J. Althoff (Leidos)</i> <i>CSAW 2019 Logic Locking Conquest @ NYU</i>
	<i>Yier Jin (UF)</i> <i>Hardware De-obfuscation Competition @ UF</i>
8:40 - 9:30PM	LL Session: CAD for Logic Locking (Westminster III) <i>Lead: Chris Taylor (AFRL)</i> <i>Scribe: Kaveh Shamsi (UF)</i> AT Session: Industrial Practices on Anti-Trojan Solutions (Westminster II) <i>Lead: Farinaz Koushanfar (UCSD)</i> <i>Scribe: Xiaolin Xu (UIC)</i>
9:30 - 9:50PM	Session Reports
9:50 - 10:00PM	Closing comments <i>Ken Plaks (DARPA)</i>